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L Number	Hits	Search Text	DB	Time stamp
1	2	6628836.pn.	USPAT; DERWENT	2004/10/14 09:54
2	4311	graphic$4 with compress$4	USPAT; DERWENT	2004/10/14 09:54
3	2	(graphic$4 with compress$4) and (setup near variable)	USPAT; DERWENT	2004/10/14 09:57
4	4	(graphic$4 with compress$4) and (setup with variable)	USPAT; DERWENT	2004/10/14 09:58
5	224	(graphic$4 with compress$4) and (setup and variable)	USPAT; DERWENT	2004/10/14 09:58
6	1	((graphic$4 with compress$4) and (setup and variable)) and (eliminat$4 with (field near type))	USPAT; DERWENT	2004/10/14 09:59
7	0	((graphic$4 with compress$4) and (setup and variable)) and ((delet$4 or remov$4) with (field near type))	USPAT; DERWENT	2004/10/14 10:00
8	28	((graphic$4 with compress$4) and (setup and variable)) and (field near type)	USPAT; DERWENT	2004/10/14 10:00
9	22	(((graphic$4 with compress$4) and (setup and variable)) and (field near type)) and packet$4	USPAT; DERWENT	2004/10/14 10:00
10	2	((((graphic$4 with compress$4) and (setup and variable)) and (field near type)) and packet$4) and geometr$4	USPAT; DERWENT	2004/10/14 10:02
11	62	duluk.in.	USPAT; DERWENT	2004/10/14 10:02
12	0	duluk,?.in.	USPAT; DERWENT	2004/10/14 10:02
13	62	duluk,.in.	USPAT; DERWENT	2004/10/14 10:03
14	0	duluk,.in. and ((((graphic$4 with compress$4) and (setup and variable)) and (field near type)) and packet$4)	USPAT; DERWENT	2004/10/14 10:03
15	0	duluk,.in. and (graphic$4 with compress$4)	USPAT; DERWENT	2004/10/14 10:03

	U	1	Document ID	Issue Date	Pages	Title	Current OR
1	☐	☐	US 6628836 B1	20030930	11	Sort middle, screen space, graphics geometry compression through redundancy elimination	382/232
2	☐	☐	US 6615338 B1	20030902	32	Clustered architecture in a VLIW processor	712/24
3	☐	☐	US 6279100 B1	20010821	48	Local stall control method and structure in a microprocessor	712/24
4	☐	☐	US 6628836 B	20030930	11	Computer graphics compression method involves modifying setup variables identified when all slope values have zero values, by eliminating slope values from variable set	

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5
1	345/501; 345/506		Wittenbrink, Craig M. et al.	☒	☐	☐	☐	☐	☐	☐
2	712/217		Tremblay, Marc et al.	☒	☐	☐	☐	☐	☐	☐
3	712/219		Tremblay, Marc et al.	☒	☐	☐	☐	☐	☐	☐
4			ORDENTLICH, E et al.	☒	☐	☐	☐	☐	☐	☐

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1	US 6628836	☐
2	US 6615338	☐
3	US 6279100	☐
4	US 6628836	☐

	U	1	Document ID	Issue Date	Pages	Title	Current OR
1	☒	☐	US 6229553 B1	20010508	23	Deferred shading graphics pipeline processor	345/506
2	☒	☐	US 6229553 B	20010508		Deferred graphic pipeline processor for three-dimension computer graphics system, has mode injection unit that maintains cache status information	
3	☒	☐	WO 200011607 A	20000302		Three-dimensional graphics processor implementing differed shading and other enhanced features during computer generation of pictures, images or other graphical or pictorial information	

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5
1	345/419; 345/426; 345/582; 345/584		Duluk, Jr., Jerome F. et al.	☐	☐	☐	☐	☐	☐	☐
2			ARNOLD, V T et al.	☐	☐	☐	☐	☐	☐	☐
3			ARNOLD, V T et al.	☐	☐	☐	☐	☐	☐	☐

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1	US 6229553	☐
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